

## **IN THE CLAIMS:**

1. (Currently Amended) A display control method for a video display system that comprises a plurality of video display apparatuses for displaying video signals being supplied as video, the display controlling method including:

a step in which a specific video display apparatus out of said plurality of video display apparatuses is selected as a master display apparatus and the other of said plurality of display apparatuses is selected as slave apparatuses;

a step in which a said specific video display apparatus ~~out of said plurality of video display apparatuses~~ discriminates video characteristics of said video signals to output identification signals that correspond to said discriminated video characteristics and simultaneously displays said video ~~identification~~ signals according to display characteristics stored in correspondence to said identification signals; and

a step in which said other video display apparatuses than said specific video display apparatus display said video signals according to said display characteristics stored in correspondence to said identification signals,

wherein said identification signals include corresponding signals regarding a frequency dividing ratio, a timing of writing into a video memory, a timing of reading from the video memory, a horizontal display width and a vertical display width at the moment of making a display, a horizontal synchronization frequency and a vertical synchronizing frequency of the input signal, polarity information of the vertical and horizontal synchronization signal, and a classification of the interlaced scanning and non-interlaced scanning.

2. (Original) The display control method for said video display system as claimed in claim 1,

wherein said video display apparatuses including:

a step for detecting said display characteristics of said video signals;

a step for reading out said identification signals that correspond to said detected video characteristics from a memory in which said video characteristics

that is to be judgment criterion and said identification signals have been stored correspondingly;

a step for reading out said display characteristics that correspond to said identification signals read out from said memory in which said identification signals and said display characteristics have been stored correspondingly; and

a step for displaying said video signals according to said read out display characteristics.

3. (Original) The display control method for said video display system as claimed in claim 2, wherein said step for displaying includes;

a step for sampling said video signals to output as each of color signals;

a step for storing in a picture memory each of said sampled color signals;

a step for producing horizontal scanning signals and vertical scanning signals based on synchronizing signal components of said video signals to output to said picture memory;

a step for reading out said each of color signals, said horizontal scanning signals, and said vertical scanning signals to produce drive signals; and

a step for displaying video based on said drive signals.

4. (Original) The display control method for said video display system as claimed in claim 3,

wherein a step for performing video adjustment for said each of color signals is further included between said step for sampling said video signals and said step for storing in said picture memory.

5. (Original) The display control method for said video display system as claimed in claim 1, wherein said video characteristics are horizontal synchronizing frequencies of said video signals.

6. (Original) The display control method for said video display system as claimed in claim 1, wherein said video characteristics are vertical synchronizing frequencies of said video signals.

7. (Original) The display control method for said video display system as claimed in claim 1, wherein said video characteristics are polarities of horizontal synchronizing components of said video signals.

8. (Original) The display control method for said video display system as claimed in claim 1, wherein said video characteristics are polarities of vertical synchronizing components of said video signals.

9. (Original) The display control method for said video display system as claimed in claim 1, wherein said video characteristics are distinctions between an interlaced scanning and a non-interlaced scanning in said video signals.

10. (Original) The display control method for said video display system as claimed in claim 1,

wherein said video characteristics are frequency dividing ratios of said video signals to a synchronizing signal components.

11. (Original) The display control method for said video display system as claimed in claim 3, wherein said video characteristics are writing timings of said video signals to said picture memory.

12. (Original) The display control method for said video display system as claimed in claim 3, wherein said video characteristics are trapping widths of said video signals that are in a horizontal direction into said picture memory.

13. (Currently Amended) The display control method for said video display system as claimed in claim 3, wherein said video characteristics are trapping widths of said video signals that are in a vertical direction into said picture memory.

14. (Original) The display control method for said video display system as claimed in claim 3, wherein said video characteristics are read-out timings of said video signals from said picture memory.

15. (Original) The display control method for said video display system as claimed in claim 3, wherein said video characteristics are display widths that are in a horizontal direction when said video signals are displayed.

16. (Original) The display control method for said video display system as claimed in claim 3, wherein said video characteristics are display widths that are in a vertical direction when said video signals are displayed.

17. (Currently Amended) A video display system comprising a plurality of video display apparatuses for displaying video signals being supplied as a video comprising;

an user interface selection means for selecting one of said ~~wherein a specific~~  
video display apparatus out of a plurality of the video display apparatuses as a master display  
apparatus; includes:

each of said plurality of display apparatuses includes,

a discriminator for discriminating the video characteristics of said video signals to  
output identification signals that correspond to the discriminated video characteristics; ~~and~~

a controller for displaying said video signals according to the video display  
characteristics stored in correspondence to said identification signals, and

wherein said discriminator only discriminates the video characteristics when its  
display apparatus is selected as the master display apparatus, and

~~wherein said other video display apparatuses than said specific video apparatuses~~  
displays include a controller for displaying said video signals according to said display  
characteristics stored in correspondence within memory in correspondence to said identification  
signals received from the master display apparatus to said identification signals master display  
apparatus and said identification signals corresponding signals regarding a frequency dividing  
ratio, a timing of writing into a video memory, a timing of reading from the video memory, a  
horizontal display width and a vertical display width at the moment of making a display, a

horizontal synchronization frequency and a vertical synchronizing frequency of the input signal, polarity information of the vertical and horizontal synchronization signal, and a classification of the interlaced scanning and non-interlaced scanning.

18. (Original) The video display system as claimed in claim 17, wherein each of said plurality of video display apparatuses includes:

a detector for detecting said video characteristics of said video signals;

a memory section of a video characteristic in which said video characteristics that are to be judgment criterion and said identification signals have been stored correspondingly;

a memory section of a display characteristic in which said identification signals and said display characteristics have been stored correspondingly;

a display controller for discriminating said identification signals that correspond to said video characteristics detected in said detector in reference to said memory section of a video characteristic to read out said display characteristics that correspond to said discriminated identification signals in reference to said memory section of a display characteristic; and

a indicator for displaying said video signals according to said display characteristics read out in said display controller.

19. (Original) The video display system as claimed in claim 18, wherein said indicator includes;

a analog-to-digital converter for sampling said video signals to output as each of color signals;

a picture memory for storing each of said sampled color signals in said analog-to-digital converter;

a control circuit of a write/read timing for producing horizontal scanning signals and vertical scanning signals based on synchronizing signal components of said video signals to output to said picture memory;

a control circuit of a display element for reading out each of said color signals, said horizontal scanning signals and said vertical scanning signals to produce drive signals; and

a display element for displaying said video based on said drive signals.

20. (Original) The video display system as claimed in claim 19, wherein a video adjusting circuit for performing video adjustment for each of said color signals is further included between said analog-to-digital converter and said picture memory.

21. (Original) The video display system as claimed in claim 17, wherein said video characteristics are horizontal synchronizing frequencies of said video signals.

22. (Original) The video display system as claimed in claim 17, wherein said video characteristics are vertical synchronizing frequencies of said video signals.

23. (Original) The video display system as claimed in claim 17, wherein said video characteristics are polarities of horizontal synchronizing components of said video signals.

24. (Original) The video display system as claimed in claim 17, wherein said video characteristics are polarities of vertical synchronizing components of said video signals.

25. (Original) The video display system as claimed in claim 17, wherein said video characteristics are distinctions between an interlaced scanning and a non-interlaced scanning of said video signals.

26. (Original) The video display system as claimed in claim 17, wherein said video characteristics are frequency dividing ratios of said video signals to synchronizing signal components.

27. (Original) The video display system as claimed in claim 19, wherein said video characteristics are writing timings of said video signals from said picture memory.

28. (Original) The d video display system as claimed in claim 19, wherein said video characteristics are display widths that are in a horizontal direction when said video signals are displayed.

29. (Original) The video display system as claimed in claim 19, wherein said video characteristics are trapping widths of said video signals that are in a vertical direction into said picture memory.

30. (Original) The video display system as claimed in claim 19, wherein said video characteristics are read-out timings of said video signals from said picture memory.

31. (Original) The video display system as claimed in claim 17, wherein said video



characteristics are display widths that are in a horizontal direction when said video signals are displayed.

32. (Original) The video display system as claimed in claim 17, wherein said video characteristics are display widths that are in a vertical direction when said video signals are displayed.

33. (Original) A video display system comprising a plurality of video display apparatuses for displaying video signals being supplied as video,

wherein each of said plurality of video display apparatuses includes:

a detector for detecting video characteristics of said video signals;

a memory section of a video characteristic in which said video characteristics that are to be criteria and said identification signals have been stored correspondingly;

a memory section of a display characteristic in which said identification signals and said display characteristics have been stored correspondingly;

a display controller for discriminating said identification signals that correspond to said video characteristics detected in said detector in reference to said memory section of a video characteristic to read out said display characteristics that correspond to said detected identification signals in reference to said memory section of a display characteristic; and

an indicator for displaying said video signals according to said display characteristics read out in said display controller,

wherein a specific video display apparatus out of said plurality of video display apparatuses detects said video characteristics of said video signals by said detector and read out

said identification signals that correspond to said detected video characteristics from said memory section of a display characteristic to output to said other video display apparatuses and simultaneously displays said video signals according to the display characteristics stored in correspondence to said identification signals, and

wherein said other apparatuses read out said display characteristics that correspond to said identification signals output from said the memory section of a display characteristic to display said video signals according to these display characteristics.

34. (Original) The video display system as claimed in claim 33, wherein said indicator includes;

a analog-to-digital converter for sampling said video signals to output as each of color signals;

a picture memory for storing each of said sampled color signals in said analog-to-digital converter;

a control circuit of a write/read timing for producing horizontal scanning signals and vertical scanning signals based on synchronizing signal components of said video signals to output to said picture memory;

a control circuit of a display element for reading out each of said color signals, said horizontal scanning signals and said vertical scanning signals to produce drive signals; and

a display element for displaying said video based on said drive signals.

35. (Original) The video display system as claimed in claim 34, wherein a video adjusting circuit for performing video adjustment for each of said color signals is further included between said analog-to-digital converter and said picture memory.

36. (Original) The video display system as claimed in claim 33, wherein said video characteristics are horizontal synchronizing frequencies of said video signals.
37. (Original) The video display system as claimed in claim 33, wherein said video characteristics are vertical synchronizing frequencies of said video signals.
38. (Original) The video display system as claimed in claim 33, wherein said video characteristics are polarities of horizontal synchronizing components of said video signals.
39. (Original) The video display system as claimed in claim 33, wherein said video characteristics are polarities of vertical synchronizing components of said video signals.
40. (Original) The video display system as claimed in claim 33, wherein said video characteristics are distinctions between an interlaced scanning and a non-interlaced scanning of said video signals.
41. (Original) The video display system as claimed in claim 33, wherein said video characteristics are frequency dividing ratios of said video signals to synchronizing signal components.
42. (Original) The video display system as claimed in claim 33, wherein said video characteristics are writing timings of said video signals from said picture memory.

43. (Original) The video display system as claimed in claim 33, wherein said video characteristics are display widths that are in a horizontal direction when said video signals are displayed.

44. (Original) The video display system as claimed in claim 33, wherein said video characteristics are trapping widths of said video signals that are in a vertical direction into said picture memory.

45. (Original) The video display system as claimed in claim 33, wherein said video characteristics are read-out timings of said video signals from said picture memory.

46. (Original) The video display system as claimed in claim 33, wherein said video characteristics are display widths that are in a horizontal direction when said video signals are displayed.

47. (Original) The video display system as claimed in claim 33, wherein said video characteristics are display widths that are in a vertical direction when said video signals are displayed.